

Amendments to the Specification:

**Please replace the paragraph beginning at page 16, line 5 with the following amended paragraph:**

A gate electrode 207 is electrically connected to the gate signal line 202. One of a source region and a drain region of the active layer 206 is connected to the source signal line 201 through a connecting wiring 209, and the other is connected to **[[a]]** the pixel electrode 208 ~~through the connecting wiring 209~~.